AO 120 (Rev. 3/04)

TO: Mail Stop 8 Director of the U.S. Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450	REPORT ON THE FILING OR DETERMINATION OF AN ACTION REGARDING A PATENT OR TRADEMARK

In Compliance with 35 U.S.C. § 290 and/or 15 U.S.C. § 1116 you are hereby advised that a court action has been filed in the U.S. District Court _____ Atlanta Division _____ on the following ☑ Patents or ☐ Trademarks:

DOCKET NO. 1:09-CV-1098-BBM	DATE FILED 4/24/2009	U.S. DISTRICT COURT Atlanta Division
PLAINTIFF Optimum Processing Solutions, L.L.C.		DEFENDANT Advanced Micro Devices, Inc. et al

PATENT OR TRADEMARK NO.	DATE OF PATENT OR TRADEMARK	HOLDER OF PATENT OR TRADEMARK
1 5,115,497	5/19/1992	Larry A. Bergman
2		
3		
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5		

In the above—entitled case, the following patent(s)/ trademark(s) have been included:

DATE INCLUDED	INCLUDED BY ☐ Amendment ☐ Answer ☐ Cross Bill ☐ Other Pleading	
PATENT OR TRADEMARK NO.	DATE OF PATENT OR TRADEMARK	HOLDER OF PATENT OR TRADEMARK
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In the above—entitled case, the following decision has been rendered or judgement issued:

DECISION/JUDGEMENT

CLERK	(BY) DEPUTY CLERK	DATE

Copy 1—Upon initiation of action, mail this copy to Director   Copy 3—Upon termination of action, mail this copy to Director
Copy 2—Upon filing document adding patent(s), mail this copy to Director   Copy 4—Case file copy

US005115497A

United States Patent [19]

Bergman

[11] Patent Number: **5,115,497**

[45] Date of Patent: **May 19, 1992**

[54] OPTICALLY INTRACONNECTED COMPUTER EMPLOYING DYNAMICALLY RECONFIGURABLE HOLOGRAPHIC OPTICAL ELEMENT

[75] Inventor: Larry A. Bergman, Van Nuys, Calif.

[73] Assignee: California Institute of Technology, Pasadena, Calif.

[21] Appl. No.: 103,192

[22] Filed: Oct. 1, 1987

[51] Int. Cl.⁵ G06F 9/00
[52] U.S. Cl. 395/378; 364/DIG. 1;
364/232.2; 364/261; 364/261.1; 364/263.1;
364/262.4; 364/262.9; 364/264
[58] Field of Search ... 364/200 MS File, 900 MS File,
364/736, 741

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Primary Examiner—Kevin A. Kriess
Attorney, Agent, or Firm—Edward O. Ansell

[57]   **ABSTRACT**

An optically intraconnected computer and a reconfigurable holographic optical element employed therein. The basic computer comprises a memory for holding a sequence of instructions to be executed; logic for accessing the instructions in sequence; logic for determining for each the instruction the function to be performed and the effective address thereof; a plurality of individual elements on a common support substrate optimized to perform certain logical sequences employed in executing the instructions; and, element selection logic connected to the logic determining the function to be performed for each the instruction for determining the class of each function and for causing the instruction to be executed by those the elements which perform those associated the logical sequences affecting the instruction execution in an optimum manner. In the optically intraconnected version, the element selection logic is adapted for transmitting and switching signals to the elements optically.

35 Claims, 4 Drawing Sheets